



The Hardware security lab at Texas A&M University, led by <u>Prof. JV Rajendran</u>, invites applications for two postdoctoral researchers in the area of trustworthy hardware design and secure electronic design automation (EDA), beginning Fall 2019. The successful candidates will work on the development and implementation of EDA techniques and algorithms for securing the hardware supply chain.

Requirements:

We are looking for self-motivated candidates with a PhD in Electrical/Computer Engineering and strong research background in the one or more of the following areas:

- 1. VLSI/EDA algorithms
- 2. Design for Test (DfT) and/or Design for Debug
- 3. Trustworthy IC design
- 4. Formal Circuit Verification

The candidates are expected to have excellent organizational and communication (oral and written) skills, ability to multitask, and work synergistically with the team. Candidates are also expected to mentor graduate and undergraduate students. Hand-on experience in IC design, EDA tools, IC testing, post-silicon debugging, computer architecture, or SoC design is a plus.

How to apply:

If you are interested in a position, please send an email with your resume to <u>jv.rajendran@tamu.edu</u>. Please also attach a one-page cover letter highlighting your key publications and experience relevant to the position.

About The Department of Electrical and Computer Engineering at Texas A&M:

The Department of Electrical and Computer Engineering at Texas A&M is ranked **10th for Computer Engineering and 12th for Electrical engineering** among public institutions by U.S. News & World Report. The department boasts close proximity to Houston/Austin and strong collaboration with the semiconductor industry in Texas. With Texas's electronics workforce being the 2nd largest in the US, the ECE students and alumni get ample chances of internships and employment in semiconductor design, manufacturing, and sales companies including Apple, Intel, Texas Instruments, ARM, and many others.

About The <u>Hardware Security Lab</u>:

The Hardware Security Lab at Texas A&M conducts research in the area of hardware security focusing on trustworthy integrated circuit (IC) design, electronic design automation (EDA) for secure systems, and security analysis of existing hardware architectures. Currently, the lab has one postdoc and five PhD students working in different aspects of hardware security. More information about our research can be found at: <u>https://cesg.tamu.edu/faculty/jv/publications</u>